

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NO. 10046893	FILING DATE 01/17/2002	CLASS 438	SUBCLASS 149	GAU 2812	EXAMINER H. Lee
**APPLICANTS: Nakamura Osamu; Yamazaki Shunpei; Dairiki Koji; Kajiwara Masayuki; Koezuka Junichi; Murakami Satoshi;					
**CONTINUING DATA VERIFIED: Lee None					
** FOREIGN APPLICATIONS VERIFIED: Lee JAPAN 2001-010890 01/18/2001 JAPAN 2001-019357 01/29/2001 Yes JAPAN 2001-022398 01/30/2001					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met			<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 740756-2424	
Verified and Acknowledged Examiner's initials Lee					
TITLE : Method of manufacturing semiconductor device					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE					
Amount Due	Date Paid				
<input type="checkbox"/> TERMINAL		Primary Examiner		DRAWING	
				Sheets Draw.	Figs. Draw.
DISCLAIMER		PREPARED FOR ISSUE		Print Fig.	
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